

L Number	Hits	Search Text	DB	Time stamp
-	144	(710/311).CCLS.	USPAT; US-PGPUB	2004/02/24 14:41
-	99	((710/311).CCLS.) and (bridge and device and (cpu or process\$4))	USPAT; US-PGPUB	2004/02/24 15:42
-	50	((710/311).CCLS.) and (bridge and device and (cpu or process\$4))) and interrupt	USPAT; US-PGPUB	2004/02/24 15:41
-	799	(710/260).CCLS.	USPAT; US-PGPUB	2004/02/24 15:41
-	95907	(bridge and device and (cpu or process\$4))	USPAT; US-PGPUB	2004/02/24 15:42
-	174	((710/260).CCLS.) and ((bridge and device and (cpu or process\$4)))	USPAT; US-PGPUB	2004/02/24 15:43
-	169	((710/260).CCLS.) and ((bridge and device and (cpu or process\$4))) and interrupt	USPAT; US-PGPUB	2004/02/24 15:43
-	106	((710/260).CCLS.) and ((bridge and device and (cpu or process\$4))) and "interrupt controller"	USPAT; US-PGPUB	2004/02/24 15:43
-	1	("5619706").PN.	USPAT; US-PGPUB	2004/02/24 17:01
-	299	(710/48).CCLS.	USPAT; US-PGPUB	2004/02/24 17:01
-	74	((710/48).CCLS.) and ((bridge and device and (cpu or process\$4)))	USPAT; US-PGPUB	2004/02/24 17:02
-	35	((710/48).CCLS.) and ((bridge and device and (cpu or process\$4))) not	USPAT; US-PGPUB	2004/02/24 17:12
-	356	((710/311).CCLS.) or ((710/260).CCLS.))	USPAT; US-PGPUB	2004/02/24 17:12
-	335	((710/306).CCLS.) not ((710/48).CCLS.) or ((710/311).CCLS.) or ((710/260).CCLS.))	USPAT; US-PGPUB	2004/02/24 17:13
-	205	((710/306).CCLS.) not ((710/48).CCLS.) or ((710/311).CCLS.) or ((710/260).CCLS.)) and ((bridge and device and (cpu or process\$4)))	USPAT; US-PGPUB	2004/02/24 17:13
-	205	((710/306).CCLS.) not ((710/48).CCLS.) or ((710/311).CCLS.) or ((710/260).CCLS.)) and ((bridge and device and (cpu or process\$4)))	USPAT; US-PGPUB	2004/02/24 17:13
-	28	((710/306).CCLS.) not ((710/48).CCLS.) or ((710/311).CCLS.) or ((710/260).CCLS.)) and ((bridge and device and (cpu or process\$4))) and "interrupt controller"	USPAT; US-PGPUB	2004/02/24 17:14
-	0	6374321.URPN.	USPAT	2004/02/25 18:40
-	4	5956516.URPN.	USPAT	2004/02/25 18:41
-	6	("4626985" "4734882" "5701496" "5727217" "5764997" "5828891").PN.	USPAT	2004/02/25 18:41

-	41	(US-6587868-\$ or US-6463483-\$ or US-6260094-\$ or US-6253275-\$ or US-6138198-\$ or US-6226700-\$ or US-6223240-\$ or US-6199134-\$ or US-6185642-\$ or US-6173351-\$ or US-6081861-\$ or US-6073198-\$ or US-6065122-\$ or US-6021456-\$ or US-5956516-\$ or US-5590338-\$ or US-6401153-\$ or US-6381665-\$ or US-6374321-\$ or US-6339808-\$ or US-5953535-\$ or US-5892956-\$ or US-5862366-\$ or US-5819095-\$ or US-5790871-\$ or US-5787290-\$).did. or (US-5764998-\$ or US-5727227-\$ or US-5721931-\$ or US-5640570-\$ or US-5619706-\$ or US-5564060-\$ or US-5857085-\$ or US-5588125-\$ or US-6604161-\$).did. or (US-20020091891-\$ or US-20010052043-\$ or US-20010032285-\$ or US-20010032286-\$ or US-20010032284-\$ or US-20010027502-\$).did.	USPAT; US-PGPUB	2004/02/26 12:26
-	3	((US-6587868-\$ or US-6463483-\$ or US-6260094-\$ or US-6253275-\$ or US-6138198-\$ or US-6226700-\$ or US-6223240-\$ or US-6199134-\$ or US-6185642-\$ or US-6173351-\$ or US-6081861-\$ or US-6073198-\$ or US-6065122-\$ or US-6021456-\$ or US-5956516-\$ or US-5590338-\$ or US-6401153-\$ or US-6381665-\$ or US-6374321-\$ or US-6339808-\$ or US-5953535-\$ or US-5892956-\$ or US-5862366-\$ or US-5819095-\$ or US-5790871-\$ or US-5787290-\$).did. or (US-5764998-\$ or US-5727227-\$ or US-5721931-\$ or US-5640570-\$ or US-5619706-\$ or US-5564060-\$ or US-5857085-\$ or US-5588125-\$ or US-6604161-\$).did. or (US-20020091891-\$ or US-20010052043-\$ or US-20010032285-\$ or US-20010032286-\$ or US-20010032284-\$ or US-20010027502-\$).did.) and ("level sensitive interrupt" or lsi)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/26 12:33
-	1	((US-6587868-\$ or US-6463483-\$ or US-6260094-\$ or US-6253275-\$ or US-6138198-\$ or US-6226700-\$ or US-6223240-\$ or US-6199134-\$ or US-6185642-\$ or US-6173351-\$ or US-6081861-\$ or US-6073198-\$ or US-6065122-\$ or US-6021456-\$ or US-5956516-\$ or US-5590338-\$ or US-6401153-\$ or US-6381665-\$ or US-6374321-\$ or US-6339808-\$ or US-5953535-\$ or US-5892956-\$ or US-5862366-\$ or US-5819095-\$ or US-5790871-\$ or US-5787290-\$).did. or (US-5764998-\$ or US-5727227-\$ or US-5721931-\$ or US-5640570-\$ or US-5619706-\$ or US-5564060-\$ or US-5857085-\$ or US-5588125-\$ or US-6604161-\$).did. or (US-20020091891-\$ or US-20010052043-\$ or US-20010032285-\$ or US-20010032286-\$ or US-20010032284-\$ or US-20010027502-\$).did.) and "read cache"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/26 12:33

-	17	((US-6587868-\$ or US-6463483-\$ or US-6260094-\$ or US-6253275-\$ or US-6138198-\$ or US-6226700-\$ or US-6223240-\$ or US-6199134-\$ or US-6185642-\$ or US-6173351-\$ or US-6081861-\$ or US-6073198-\$ or US-6065122-\$ or US-6021456-\$ or US-5956516-\$ or US-5590338-\$ or US-6401153-\$ or US-6381665-\$ or US-6374321-\$ or US-6339808-\$ or US-5953535-\$ or US-5892956-\$ or US-5862366-\$ or US-5819095-\$ or US-5790871-\$ or US-5787290-\$).did. or (US-5764998-\$ or US-5727227-\$ or US-5721931-\$ or US-5640570-\$ or US-5619706-\$ or US-5564060-\$ or US-5857085-\$ or US-5588125-\$ or US-6604161-\$).did. or (US-20020091891-\$ or US-20010052043-\$ or US-20010032285-\$ or US-20010032286-\$ or US-20010032284-\$ or US-20010027502-\$).did.) and cache	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/26 12:33
-	41	((US-6587868-\$ or US-6463483-\$ or US-6260094-\$ or US-6253275-\$ or US-6138198-\$ or US-6226700-\$ or US-6223240-\$ or US-6199134-\$ or US-6185642-\$ or US-6173351-\$ or US-6081861-\$ or US-6073198-\$ or US-6065122-\$ or US-6021456-\$ or US-5956516-\$ or US-5590338-\$ or US-6401153-\$ or US-6381665-\$ or US-6374321-\$ or US-6339808-\$ or US-5953535-\$ or US-5892956-\$ or US-5862366-\$ or US-5819095-\$ or US-5790871-\$ or US-5787290-\$).did. or (US-5764998-\$ or US-5727227-\$ or US-5721931-\$ or US-5640570-\$ or US-5619706-\$ or US-5564060-\$ or US-5857085-\$ or US-5588125-\$ or US-6604161-\$).did. or (US-20020091891-\$ or US-20010052043-\$ or US-20010032285-\$ or US-20010032286-\$ or US-20010032284-\$ or US-20010027502-\$).did.)	USPAT; US-PGPUB	2004/02/26 13:28
-	6	((US-6587868-\$ or US-6463483-\$ or US-6260094-\$ or US-6253275-\$ or US-6138198-\$ or US-6226700-\$ or US-6223240-\$ or US-6199134-\$ or US-6185642-\$ or US-6173351-\$ or US-6081861-\$ or US-6073198-\$ or US-6065122-\$ or US-6021456-\$ or US-5956516-\$ or US-5590338-\$ or US-6401153-\$ or US-6381665-\$ or US-6374321-\$ or US-6339808-\$ or US-5953535-\$ or US-5892956-\$ or US-5862366-\$ or US-5819095-\$ or US-5790871-\$ or US-5787290-\$).did. or (US-5764998-\$ or US-5727227-\$ or US-5721931-\$ or US-5640570-\$ or US-5619706-\$ or US-5564060-\$ or US-5857085-\$ or US-5588125-\$ or US-6604161-\$).did. or (US-20020091891-\$ or US-20010052043-\$ or US-20010032285-\$ or US-20010032286-\$ or US-20010032284-\$ or US-20010027502-\$).did.) and ((cpu or processor) near6 ((deassert or flag or status) near3 interrupt))	USPAT; US-PGPUB	2004/02/26 13:37

-	6	((US-6587868-\$ or US-6463483-\$ or US-6260094-\$ or US-6253275-\$ or US-6138198-\$ or US-6226700-\$ or US-6223240-\$ or US-6199134-\$ or US-6185642-\$ or US-6173351-\$ or US-6081861-\$ or US-6073198-\$ or US-6065122-\$ or US-6021456-\$ or US-5956516-\$ or US-5590338-\$ or US-6401153-\$ or US-6381665-\$ or US-6374321-\$ or US-6339808-\$ or US-5953535-\$ or US-5892956-\$ or US-5862366-\$ or US-5819095-\$ or US-5790871-\$ or US-5787290-\$).did. or (US-5764998-\$ or US-5727227-\$ or US-5721931-\$ or US-5640570-\$ or US-5619706-\$ or US-5564060-\$ or US-5857085-\$ or US-5588125-\$ or US-6604161-\$).did. or (US-20020091891-\$ or US-20010052043-\$ or US-20010032285-\$ or US-20010032286-\$ or US-20010032284-\$ or US-20010027502-\$).did.) and ((cpu or processor) near6 ((deassert\$5 or flag or status) near3 interrupt))	USPAT; US-PGPUB	2004/02/26 13:43
-	4	processor near3 (deassert near3 interrupt)	USPAT; US-PGPUB	2004/02/26 16:31
-	1946	"programmed IO" or PIO	USPAT; US-PGPUB	2004/02/26 17:02
-	41	("programmed IO" or PIO) and (eoi or "end of interrupt")	USPAT; US-PGPUB	2004/02/26 16:40
-	3	6279064.URPN.	USPAT	2004/02/26 16:56
-	11	("4271468" "4488227" "5367689" "5410710" "5671446" "5675807" "5689713" "5692200" "5721931" "5771387" "5943500").PN.	USPAT	2004/02/26 16:56
-	44	(US-6587868-\$ or US-6463483-\$ or US-6260094-\$ or US-6253275-\$ or US-6138198-\$ or US-6226700-\$ or US-6223240-\$ or US-6199134-\$ or US-6185642-\$ or US-6173351-\$ or US-6081861-\$ or US-6073198-\$ or US-6065122-\$ or US-6021456-\$ or US-5956516-\$ or US-5590338-\$ or US-6401153-\$ or US-6381665-\$ or US-6374321-\$ or US-6339808-\$ or US-5953535-\$ or US-5892956-\$ or US-5862366-\$ or US-5819095-\$ or US-5790871-\$ or US-5787290-\$).did. or (US-5764998-\$ or US-5727227-\$ or US-5721931-\$ or US-5640570-\$ or US-5619706-\$ or US-5564060-\$ or US-5857085-\$ or US-5588125-\$ or US-6604161-\$ or US-6442634-\$ or US-6279064-\$ or US-6065088-\$).did. or (US-20020091891-\$ or US-20010052043-\$ or US-20010032285-\$ or US-20010032286-\$ or US-20010032284-\$ or US-20010027502-\$).did.	USPAT; US-PGPUB	2004/02/26 17:00

-	6	((US-6587868-\$ or US-6463483-\$ or US-6260094-\$ or US-6253275-\$ or US-6138198-\$ or US-6226700-\$ or US-6223240-\$ or US-6199134-\$ or US-6185642-\$ or US-6173351-\$ or US-6081861-\$ or US-6073198-\$ or US-6065122-\$ or US-6021456-\$ or US-5956516-\$ or US-5590338-\$ or US-6401153-\$ or US-6381665-\$ or US-6374321-\$ or US-6339808-\$ or US-5953535-\$ or US-5892956-\$ or US-5862366-\$ or US-5819095-\$ or US-5790871-\$ or US-5787290-\$).did. or (US-5764998-\$ or US-5727227-\$ or US-5721931-\$ or US-5640570-\$ or US-5619706-\$ or US-5564060-\$ or US-5857085-\$ or US-5588125-\$ or US-6604161-\$ or US-6442634-\$ or US-6279064-\$ or US-6065088-\$).did. or (US-20020091891-\$ or US-20010052043-\$ or US-20010032285-\$ or US-20010032286-\$ or US-20010032284-\$ or US-20010027502-\$).did.) and ("programmed IO" or PIO)	USPAT; US-PGPUB	2004/02/26 17:00
-	2269	"programmed IO" or PIO or "programed input/output" or "programmed I/O"	USPAT; US-PGPUB	2004/02/26 17:03
-	43	("programmed IO" or PIO or "programed input/output" or "programmed I/O") and (eoi or "end of interrupt")	USPAT; US-PGPUB	2004/02/26 17:03
-	2	((("programmed IO" or PIO or "programed input/output" or "programmed I/O") and (eoi or "end of interrupt"))) not (("programmed IO" or PIO) and (eoi or "end of interrupt"))	USPAT; US-PGPUB	2004/02/26 17:05
-	1461	(710/311,260,48,306).CCLS.	USPAT; US-PGPUB	2004/02/26 17:07
-	0	((710/311,260,48,306).CCLS.) and "end of interrupt"	USPAT; US-PGPUB	2004/02/26 17:06
-	1461	((710/311) or (710/260) or (710/48) or (710/306)).CCLS.	USPAT; US-PGPUB	2004/02/26 17:07
-	0	((710/311) or (710/260) or (710/48) or (710/306)).CCLS.) and "end of interrupt"	USPAT; US-PGPUB	2004/02/26 17:08
-	80	((710/311) or (710/260) or (710/48) or (710/306)).CCLS.) and ("end of interrupt" or eoi or end-of-interrupt)	USPAT; US-PGPUB	2004/02/26 17:08
-	36	((710/311) or (710/260) or (710/48) or (710/306)).CCLS.) and end-of-interrupt	USPAT; US-PGPUB	2004/02/26 17:53
-	2	((US-6587868-\$ or US-6463483-\$ or US-6260094-\$ or US-6253275-\$ or US-6138198-\$ or US-6226700-\$ or US-6223240-\$ or US-6199134-\$ or US-6185642-\$ or US-6173351-\$ or US-6081861-\$ or US-6073198-\$ or US-6065122-\$ or US-6021456-\$ or US-5956516-\$ or US-5590338-\$ or US-6401153-\$ or US-6381665-\$ or US-6374321-\$ or US-6339808-\$ or US-5953535-\$ or US-5892956-\$ or US-5862366-\$ or US-5819095-\$ or US-5790871-\$ or US-5787290-\$).did. or (US-5764998-\$ or US-5727227-\$ or US-5721931-\$ or US-5640570-\$ or US-5619706-\$ or US-5564060-\$ or US-5857085-\$ or US-5588125-\$ or US-6604161-\$ or US-6442634-\$ or US-6279064-\$ or US-6065088-\$).did. or (US-20020091891-\$ or US-20010052043-\$ or US-20010032285-\$ or US-20010032286-\$ or US-20010032284-\$ or US-20010027502-\$).did.) and (lsi or "level senstive interrupt")	USPAT; US-PGPUB USPAT; US-PGPUB	2004/02/26 17:54

-	4	((US-6587868-\$ or US-6463483-\$ or US-6260094-\$ or US-6253275-\$ or US-6138198-\$ or US-6226700-\$ or US-6223240-\$ or US-6199134-\$ or US-6185642-\$ or US-6173351-\$ or US-6081861-\$ or US-6073198-\$ or US-6065122-\$ or US-6021456-\$ or US-5956516-\$ or US-5590338-\$ or US-6401153-\$ or US-6381665-\$ or US-6374321-\$ or US-6339808-\$ or US-5953535-\$ or US-5892956-\$ or US-5862366-\$ or US-5819095-\$ or US-5790871-\$ or US-5787290-\$).did. or (US-5764998-\$ or US-5727227-\$ or US-5721931-\$ or US-5640570-\$ or US-5619706-\$ or US-5564060-\$ or US-5857085-\$ or US-5588125-\$ or US-6604161-\$ or US-6442634-\$ or US-6279064-\$ or US-6065088-\$).did. or (US-20020091891-\$ or US-20010052043-\$ or US-20010032285-\$ or US-20010032286-\$ or US-20010032284-\$ or US-20010027502-\$).did.) and (lsi or "level sensitive")	USPAT; US-PGPUB	2004/02/26 17:56
-	1	((US-6587868-\$ or US-6463483-\$ or US-6260094-\$ or US-6253275-\$ or US-6138198-\$ or US-6226700-\$ or US-6223240-\$ or US-6199134-\$ or US-6185642-\$ or US-6173351-\$ or US-6081861-\$ or US-6073198-\$ or US-6065122-\$ or US-6021456-\$ or US-5956516-\$ or US-5590338-\$ or US-6401153-\$ or US-6381665-\$ or US-6374321-\$ or US-6339808-\$ or US-5953535-\$ or US-5892956-\$ or US-5862366-\$ or US-5819095-\$ or US-5790871-\$ or US-5787290-\$).did. or (US-5764998-\$ or US-5727227-\$ or US-5721931-\$ or US-5640570-\$ or US-5619706-\$ or US-5564060-\$ or US-5857085-\$ or US-5588125-\$ or US-6604161-\$ or US-6442634-\$ or US-6279064-\$ or US-6065088-\$).did. or (US-20020091891-\$ or US-20010052043-\$ or US-20010032285-\$ or US-20010032286-\$ or US-20010032284-\$ or US-20010027502-\$).did.) and ("edge sensitive")	USPAT; US-PGPUB	2004/02/26 17:58
-	0	((US-6587868-\$ or US-6463483-\$ or US-6260094-\$ or US-6253275-\$ or US-6138198-\$ or US-6226700-\$ or US-6223240-\$ or US-6199134-\$ or US-6185642-\$ or US-6173351-\$ or US-6081861-\$ or US-6073198-\$ or US-6065122-\$ or US-6021456-\$ or US-5956516-\$ or US-5590338-\$ or US-6401153-\$ or US-6381665-\$ or US-6374321-\$ or US-6339808-\$ or US-5953535-\$ or US-5892956-\$ or US-5862366-\$ or US-5819095-\$ or US-5790871-\$ or US-5787290-\$).did. or (US-5764998-\$ or US-5727227-\$ or US-5721931-\$ or US-5640570-\$ or US-5619706-\$ or US-5564060-\$ or US-5857085-\$ or US-5588125-\$ or US-6604161-\$ or US-6442634-\$ or US-6279064-\$ or US-6065088-\$).did. or (US-20020091891-\$ or US-20010052043-\$ or US-20010032285-\$ or US-20010032286-\$ or US-20010032284-\$ or US-20010027502-\$).did.) and ("edge-sensitive")	USPAT; US-PGPUB	2004/02/26 17:59

-	14	((US-6587868-\$ or US-6463483-\$ or US-6260094-\$ or US-6253275-\$ or US-6138198-\$ or US-6226700-\$ or US-6223240-\$ or US-6199134-\$ or US-6185642-\$ or US-6173351-\$ or US-6081861-\$ or US-6073198-\$ or US-6065122-\$ or US-6021456-\$ or US-5956516-\$ or US-5590338-\$ or US-6401153-\$ or US-6381665-\$ or US-6374321-\$ or US-6339808-\$ or US-5953535-\$ or US-5892956-\$ or US-5862366-\$ or US-5819095-\$ or US-5790871-\$ or US-5787290-\$).did. or (US-5764998-\$ or US-5727227-\$ or US-5721931-\$ or US-5640570-\$ or US-5619706-\$ or US-5564060-\$ or US-5857085-\$ or US-5588125-\$ or US-6604161-\$ or US-6442634-\$ or US-6279064-\$ or US-6065088-\$).did. or (US-20020091891-\$ or US-20010052043-\$ or US-20010032285-\$ or US-20010032286-\$ or US-20010032284-\$ or US-20010027502-\$).did.) and (edge near6 interrupt)	USPAT; US-PGPUB	2004/02/26 18:00
-	26	((US-6587868-\$ or US-6463483-\$ or US-6260094-\$ or US-6253275-\$ or US-6138198-\$ or US-6226700-\$ or US-6223240-\$ or US-6199134-\$ or US-6185642-\$ or US-6173351-\$ or US-6081861-\$ or US-6073198-\$ or US-6065122-\$ or US-6021456-\$ or US-5956516-\$ or US-5590338-\$ or US-6401153-\$ or US-6381665-\$ or US-6374321-\$ or US-6339808-\$ or US-5953535-\$ or US-5892956-\$ or US-5862366-\$ or US-5819095-\$ or US-5790871-\$ or US-5787290-\$).did. or (US-5764998-\$ or US-5727227-\$ or US-5721931-\$ or US-5640570-\$ or US-5619706-\$ or US-5564060-\$ or US-5857085-\$ or US-5588125-\$ or US-6604161-\$ or US-6442634-\$ or US-6279064-\$ or US-6065088-\$).did. or (US-20020091891-\$ or US-20010052043-\$ or US-20010032285-\$ or US-20010032286-\$ or US-20010032284-\$ or US-20010027502-\$).did.) and (level near6 interrupt)	USPAT; US-PGPUB	2004/02/26 18:58
-	36	((US-6587868-\$ or US-6463483-\$ or US-6260094-\$ or US-6253275-\$ or US-6138198-\$ or US-6226700-\$ or US-6223240-\$ or US-6199134-\$ or US-6185642-\$ or US-6173351-\$ or US-6081861-\$ or US-6073198-\$ or US-6065122-\$ or US-6021456-\$ or US-5956516-\$ or US-5590338-\$ or US-6401153-\$ or US-6381665-\$ or US-6374321-\$ or US-6339808-\$ or US-5953535-\$ or US-5892956-\$ or US-5862366-\$ or US-5819095-\$ or US-5790871-\$ or US-5787290-\$).did. or (US-5764998-\$ or US-5727227-\$ or US-5721931-\$ or US-5640570-\$ or US-5619706-\$ or US-5564060-\$ or US-5857085-\$ or US-5588125-\$ or US-6604161-\$ or US-6442634-\$ or US-6279064-\$ or US-6065088-\$).did. or (US-20020091891-\$ or US-20010052043-\$ or US-20010032285-\$ or US-20010032286-\$ or US-20010032284-\$ or US-20010027502-\$).did.) and (cache or buffer or queue)	USPAT; US-PGPUB	2004/02/26 19:13

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'interrupt controller'

Search

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1 A self timed interrupt controller: a case study in asynchronous microarchitecture design

de Gloria, A.; Faraboschi, P.; Olivieri, M.;

ASIC Conference and Exhibit, 1994. Proceedings., Seventh Annual IEEE International , 19-23 Sept. 1994
Pages:296 - 299

[\[Abstract\]](#) [\[PDF Full-Text \(392 KB\)\]](#) **IEEE CNE**

2 Design and implementation of interrupt packaging mechanism

Nakashima, K.; Kusakabe, S.; Taniguchi, H.; Amamiya, M.;

Innovative Architecture for Future Generation High-Performance Processors and Systems, 2002. International Workshop on, 10-11 Jan. 2002
Pages:95 - 102

[\[Abstract\]](#) [\[PDF Full-Text \(358 KB\)\]](#) **IEEE CNF**

3 A high-precision timing and interrupt controller to support distributed real-time operating systems

Halang, W.A.; Wannemacher, M.; Pereira, C.E.;

Circuits and Systems, 1995., Proceedings., Proceedings of the 38th Midwest Symposium on , Volume: 1 , 13-16 Aug. 1995
Pages:9 - 12 vol.1

[Abstract] [PDF Full-Text (336 KB)] IEEE CNE

4 IBM single chip RISC processor (RSC)

Moore, C.R.; Balser, D.M.; Muhich, J.S.; East, R.E.;

Computer Design: VLSI in Computers and Processors, 1992. ICCD '92. Proceedings., IEEE 1992 International Conference on , 11-14 Oct. 1992

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1 A methodology for the verification of a "system on chip" 80%



Daniel Geist , Giora Biran , Tamara Arons , Michael Slavkin , Yvgeny Nustov , Monica Farkas , Karen Holtz , Andy Long , Dave King , Steve Barret

Proceedings of the 36th ACM/IEEE conference on Design automation conference
June 1999

2 Work-in-progress session on innovative topics: SEAS: a system for early 77%
analysis of SoCs



Reinaldo A. Bergamaschi , Youngsoo Shin , Nagu Dhanwada , Subhrajit Bhattacharya , William E. Dougherty , Indira Nair , John Darringer , Sarala Paliwal

Proceedings of the 1st IEEE/ACM/IFIP international conference on Hardware/software codesign & system synthesis October 2003

Systems-on-chip (SoC) continue to be very complex to design and verify, despite extensive component reuse. Although reusable components are pre-designed and pre-verified, when they are assembled in an SoC there is no guarantee that the whole system will behave as expected from a performance, cost and integration point of view. In many cases this is because of faulty early design decisions regarding the architecture, core selection, floorplanning, etc. This paper presents a system for early analy ...

3 A survey of processors with explicit multithreading 77%



Theo Ungerer , Borut Robič , Jurij Šilc

ACM Computing Surveys (CSUR) March 2003
Volume 35 Issue 1

Hardware multithreading is becoming a generally applied technique in the next generation of microprocessors. Several multithreaded processors are announced by industry or already into production in the areas of high-performance microprocessors, media, and network processors. A multithreaded processor is able to pursue two or more threads of control in parallel within the processor pipeline. The contexts of two or more threads of control are often stored in separate on-chip register sets. Unused i ...



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1 A simulation-based backward planning approach for order-release 80%
 Edward F. Watson , Deborah J. Medeiros , Randall P. Sadowski
Proceedings of the 29th conference on Winter simulation December 1997

2 MASCOT: a specification and cosimulation method integrating data and 77%
 control flow
 Per Bjur  s , Axel Jantsch
Proceedings of the conference on Design, automation and test in Europe January 2000

3 Generating component release plans with backward simulation 77%
 Edward F. Watson , Deborah J. Medeiros , Randall P. Sadowski
Proceedings of the 25th conference on Winter simulation December 1993

4 On the correctness of orphan management algorithms 77%
 Maurice Herlihy , Nancy Lynch , Michael Merritt , William Weihl
Journal of the ACM (JACM) October 1992
 Volume 39 Issue 4

In a distributed system, node failures, network delays, and other unpredictable occurrences can result in orphan computations—subcomputations that continue to run but whose results are no longer needed. Several algorithms have been proposed to prevent such computations from seeing inconsistent states of the shared data. In this paper, two such orphan management algorithms are analyzed. The first is an algorithm implemented in the Argus distributed-computing system at M ...

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1 The click modular router 85%



Eddie Kohler , Robert Morris , Benjie Chen , John Jannotti , M. Frans Kaashoek

ACM Transactions on Computer Systems (TOCS) August 2000

Volume 18 Issue 3

Clicks is a new software architecture for building flexible and configurable routers. A Click router is assembled from packet processing modules called elements. Individual elements implement simple router functions like packet classification, queuing, scheduling, and interfacing with network devices. A router configurable is a directed graph with elements at the vertices; packets flow along the edges of the graph. Several features make individual elements more powerful and ...

2 The Click modular router 85%



Robert Morris , Eddie Kohler , John Jannotti , M. Frans Kaashoek

ACM SIGOPS Operating Systems Review , Proceedings of the seventeenth ACM symposium on Operating systems principles December 1999

Volume 33 Issue 5

Click is a new software architecture for building flexible and configurable routers. A Click router is assembled from packet processing modules called *elements*. Individual elements implement simple router functions like packet classification, queueing, scheduling, and interfacing with network devices. Complete configurations are built by connecting elements into a graph; packets flow along the graph's edges. Several features make individual elements more powerful and complex configuration ...

3 Distributed operating systems 82%



Andrew S. Tanenbaum , Robbert Van Renesse

ACM Computing Surveys (CSUR) December 1985

Volume 17 Issue 4



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1 Tool support for architectural decisions in embedded systems: 93%

Automatic communication refinement for system level design

Samar Abdi , Dongwan Shin , Daniel Gajski

Proceedings of the 40th conference on Design automation June 2003

This paper presents a methodology and algorithms for automatic communication refinement. The communication refinement task in system-level synthesis transforms abstract data-transfer between components to its actual bus level implementation. The input model of the communication refinement is a set of concurrently executing components, communicating with each other through abstract communication channels. The refined model reflects the actual communication architecture. Choosing a good communicat ...

2 A survey of commercial parallel processors 84%

Edward Gehringer , Janne Abullarade , Michael H. Gulyin

ACM SIGARCH Computer Architecture News September 1988

Volume 16 Issue 4

This paper compares eight commercial parallel processors along several dimensions. The processors include four shared-bus multiprocessors (the Encore Multimax, the Sequent Balance system, the Alliant FX series, and the ELXSI System 6400) and four network multiprocessors (the BBN Butterfly, the NCUBE, the Intel iPSC/2, and the FPS T Series). The paper contrasts the computers from the standpoint of interconnection structures, memory configurations, and interprocessor communication. Also, the share ...

3 Methodology for hardware/software co-verification in C/C++ (short paper) 82%

Luc Séméria , Abhijit Ghosh

Proceedings of the 2000 conference on Asia South Pacific design automation